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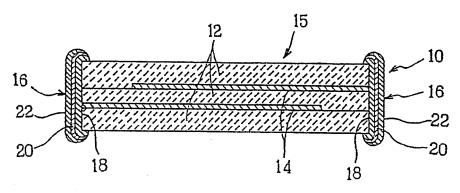
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- Ceramic capacitor and method for the preparation thereof.
- Disclosed is a ceramic capacitor of a monolayer structure or a multi-layer structure, which has characteristic X7R (characteristic B) and which is high in reliability in direct current breakdown voltage at 125 °C and small in size. The ceramic capacitor has a dielectric layer (12) and internal electrodes (14) disposed so as to interpose the dielectric layer. The dielectric layer (12) is composed of a sintered body of ceramic particles whose core sections are made mainly of BaTiO<sub>3</sub> and whose shell sections are made from BaTiO<sub>3</sub> in the form of a solid solution composed of an oxide of one or more than one element selected from Mg, Ca, Sr and Zn and an oxide of one or more than one element selected from Sc, Y, Gd, Dy, Ho, Er, Yb, Tb, Tm and Lu.

FIG. 4



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#### BACKGROUND OF THE INVENTION

#### Field of the Invention

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The present invention relates to a ceramic capacitor and a method for the preparation of the ceramic capacitor and, more particularly, to a ceramic capacitor of a mono-layer structure or of a multi-layer structure, having one or more than one dielectric layer composed of a dielectric ceramic composition containing BaTiO<sub>3</sub> as a major component and two or more than two internal electrodes so disposed as to interpose the dielectric layer.

### 2. Description of the Related Art

Ceramic capacitors of a multi-layer structure are generally formed by alternately laminating non-sintered ceramic sheets for forming dielectric layers and conductive paste films and baking the laminated dielectric layers and conductive paste films at temperatures ranging from 1,300 °C to 1,600 °C in oxidative atmosphere. Hence, such expensive metals as failing to be oxidized upon baking in oxidative atmosphere, such as platinum or palladium, should be employed as material for the internal electrode, thereby making costs of ceramic capacitors expensive.

In order to reduce the costs of the ceramic capacitors, it is considered to use a base metal, such as Ni or the like, as the material for the internal electrodes and to bake the base metal in reductive atmosphere to prevent the oxidation of the base metal. It is to be noted, however, that desired dielectric ceramic compositions having sufficiently high electrical characteristics cannot be produced by baking material of the dielectric ceramic compositions so far known in reductive atmosphere.

For example, Japanese Patent Laid-open Publication (kokai) No. 61-275,164 discloses dielectric ceramic compositions having a high dielectric constant yet a small rate of change of temperatures of capacitance which can be produced by baking powder of raw material obtainable by adding  $Nb_2O_5$  and  $Co_2O_3$  to  $BaTiO_3$  in oxidative atmosphere. It is to be noted, however, that the baking of such powder of the raw material in such reductive atmosphere cannot produce the dielectric ceramic composition having desired electrical characteristics because the baked body may become readily converted into semi-conductors.

Japanese Patent Publication (kokoku) Nos. 61-14,607-14,611 and 62-1,595-1,596 propose dielectric ceramic compositions, obtainable by baking in reductive atmosphere, having a high dielectric constant yet a small rate of change in temperatures of the capacitance. Further, our Japanese Patent Application Nos. 1-311,094-98, 2-314,754-56, 1-315,981-83, 2-16,345, 2-76,761-68 and 2-76,773-776 propose improvements in various electric characteristics of the dielectric ceramic compositions.

The dielectric ceramic compositions as proposed by those patent applications as described hereinabove contain, as a first basic component, a component as represented by  $(Ba_{k\cdot x}M_x)O_k(Ti_{1\cdot y}R_y)O_{2\cdot y/2}$ . As a second basic component, there may optionally be employed a component as represented by  $CaZrO_3$  or  $BaZrO_3$ . Further, as a first additive component, there may be employed  $Li_2O-SiO_2-MeO$  or  $B_2O_3-SiO_2-MeO$ , while at least one of  $Cr_2O_3$  and  $Al_2O_3$  may optionally be employed as a second additive component.

Recently, however, very strong demands have been made to minimize laminated ceramic capacitors as electronic circuit becomes highly dense. As making laminated ceramic capacitors compact, there are known many processes for thinning the thickness of the dielectric layers and for increasing the number of layers to be laminated. If the thickness of the dielectric layer becomes too thin, however, the direct current breakdown voltage may deviate and reliability in the withstand voltage of the ceramic capacitor cannot be maintained.

For instance, for laminated ceramic capacitors requiring for characteristic X7R (characteristic B), reliability in the direct current breakdown voltage particularly at the highest temperature as high as 125°C places particular significance. However, the thickness of the dielectric ceramic compositions disclosed in the aforesaid patent applications become too thin, leading to deviation in the direct current breakdown voltage at 125 °C and making it impossible to maintain the reliability in the withstand voltage at that temperature.

#### SUMMARY OF THE INVENTION

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The object of the present invention is to provide ceramic capacitors of a mono-layer structure or of a multi-layer structure and a method for the preparation of small-sized ceramic capacitors having high reliability in the direct current breakdown voltage at 125 °C.

Other objects, features and advantages of the present invention will become apparent in the course of the description of the preferred embodiments, which follows, with reference to the accompanying drawings.

### BRIEF DESCRIPTION OF THE DRAWINGS

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- Fig. 1 is an electron microphotograph showing a dielectric ceramic composition according to Example 1.
- Fig. 2 is a graph showing an EDS spectrum of a core section of a ceramic particle of the dielectric ceramic composition according to Example 1.
- Fig. 3 is a graph showing an EDS spectrum of a shell section of a ceramic particle of the dielectric ceramic composition according to Example 1.
  - Fig. 4 is a sectional view showing the ceramic capacitor according to Example 1.
  - Fig. 5 is an electron microphotograph showing a sample of Comparative Example.
  - Fig. 6 is a graph showing an EDS spectrum of the section A of Fig. 5.
  - Fig. 7 is a graph showing an EDS spectrum of the section B of Fig. 5.

# DESCRIPTION OF THE PREFERRED EMBODIMENTS

The ceramic capacitor according to the present invention has a dielectric layer composed of one or more than one dielectric ceramic composition and two or more than two internal electrodes formed so as to interpose the dielectric layer. The dielectric ceramic composition is composed of a sintered body of a ceramic particle whose core section is composed mainly of BaTiO<sub>3</sub> and whose shell section is composed of BaTiO<sub>3</sub> in the form of a solid solution containing an oxide of one or more than one element selected from Mg, Ca, Sr and Zn and an oxide of one or more than one element selected from Sc, Y, Gd, Dy, Ho, Er, Yb, Tb, Tm and Lu.

The core section of the ceramic particle is intended to mean an inner section of the ceramic particle and the shell section of the ceramic particle is intended to mean an outer shell section thereof. The shell section of the particle particle may optionally be composed of a solid solution of ZrO<sub>2</sub>. Further, the internal electrode may be composed of conductive material containing Ni as a major component or composed of another metal.

The method for the preparation of the ceramic capacitor according to the present invention comprises a step of preparing powder of the raw material as the basic component, a step of calcining the powder of the raw material at a predetermined temperature, a step of forming a slurry by mixing the calcined raw material powder of the basic component with powder of the additive component and an organic binder, a step of forming the slurry in the form of a sheet to give a non-sintered ceramic sheet and formulating a laminated body by forming a conductive paste film so as to interpose the non-sintered ceramic sheet, a step of baking the laminated body in a non-oxidative atmosphere, and a step of heating the laminated body in an oxidative atmosphere. The basic component may be composed of BaTiO<sub>3</sub> as a major component, one or more than one oxide selected from MgO, CaO, SrO and ZnO, an oxide as represented by R<sub>2</sub>O<sub>3</sub> (wherein R is one or more than one elements selected from Sc, Y, Gd, Dy, Ho, Er, Yb, Tb, Tm and Lu), and BaO and/or TiO<sub>2</sub> which are/is added so as to amount to  $1.00 \le k \le 1.04$  (wherein k is a ratio by mole of (Ba + Mg + Ca + Sr + Zn) to (Ti + R), i.e. (Ba + Mg + Ca + Sr + Zn)/(Ti + R)). The additive component is composed of Li<sub>2</sub>O-SiO<sub>2</sub>-MeO or B<sub>2</sub>O<sub>3</sub>-SiO<sub>2</sub>-MeO (wherein Me is one or more than one element selected from Ba, Ca, Sr, Mg and Zn).

An alternative method for the preparation of the ceramic capacitor involves a step of preparing powder of the raw material as the first basic component, a step of calcining the powder of the raw material of the first component at a predetermined temperature, a step of forming a slurry by mixing the calcined raw material powder of the first basic component with powder of the second basic component, powder of an additive component and an organic binder, a step of forming the slurry in the form of a sheet to give a non-sintered ceramic sheet and formulating a laminated body by forming a conductive paste film so as to interpose the non-sintered ceramic sheet, a step of baking the laminated body in a non-oxidative atmosphere, and a step of heating the laminated body in an oxidative atmosphere. The first basic component may be composed of BaTiO3 as a major component, one or more than one oxide selected from MgO, CaO, SrO and ZnO, powder of an oxide as represented by R2O3 (wherein R is one member selected from one or more than one element selected from Sc, Y, Gd, Dy, Ho, Er, Yb, Tb, Tm and Lu), and BaO and/or TiO2 which are/is added so as to amount to  $1.00 \le k \le 1.04$  (wherein k is a ratio by mole of (Ba + Mg + Ca + Sr + Zn) to (Ti + R)). The second basic component is composed of CaZrO3 and/or BaZrO3. The additive component is composed of Li2O-SiO2-MeO or B2O3-SiO2-MeO (wherein Me is one or

more than one element selected from Ba, Ca, Sr, Mg and Zn).

In the two methods for the preparation of the ceramic capacitors as described hereinabove, the raw material of the basic component is not restricted to the oxides and it may include any compound, such as a carbonate or the like, that can be converted into an oxide by baking. The additive component may include ones containing optionally  $Cr_2O_3$  and/or  $Al_2O_3$ . Although the conductive paste may be formed with material containing Ni as a major component, a metal other than Ni may also be employed.

In the two methods as described hereinabove, the temperatures at which the first basic component is calcined may preferably range from  $800\,^{\circ}$ C to  $1,100\,^{\circ}$ C. If the calcining temperature would be lower than  $800\,^{\circ}$ C, on the one hand, the decomposition of  $BaCO_3$  or the like proceeds to an insufficient extent and the reaction is likely to be carried out in an irregular fashion. If the calcining temperature would exceed  $1,100\,^{\circ}$ C, on the other hand, the growth of particles and aggregation would start, thereby causing a decrease in the sintering ability.

It is preferred to use  $BaTiO_3$  as the first basic component in the purity as high as 99% or higher and having an average particle size ranging from 0.5 micron to 2.0 microns. If the particle size would be smaller than 0.5 micron, the reaction may proceed too much, thereby converting the first basic component into a solid solution. If the particle size would become larger than 2.0 microns, the sintering ability may be decreased. The reason for setting the ratio by mole (k = (Ba + Mg + Ca + Sr + Zn)/(Ti + R)) to  $1.00 \le k \le 1.04$  is because the dielectric ceramic composition is converted into semi-conductor when k is smaller than 1.00, on the one hand, and the dielectric body suffers from the difficulty of becoming dense when k is larger than 1.04, on the other hand.

In conventional methods for the preparation of ceramic capacitors, all components of raw material are mixed together to carry out reaction concurrently when the first basic component as represented by  $(Ba_{k-x}M_x)O_{K-}(Ti_{1-y}R_y)O_{2-y/2}$ , thereby making the reaction likely to occur in an irregular fashion as well as localizing particularly small components, i.e. a component M (such as Ca, Sr, Mg or Zn) and a component R (such as Sc, Y, Gd, Dy, Ho, Er, Yb, Tb, Tm or Lu) in the dielectric ceramic composition and deviating from the composition to a great extent.

On the other hand, the present invention can suppress the composition of the components M and R from deviating by carrying out the reaction uniformly between  $BaTiO_3$  and the additive components because Ba, the component M, Ti, and the component R are added to the previously synthesized  $BaTiO_3$  at predetermined rates and heating the mixture.

The present invention will be described more in detail by way of examples.

### Example 1:

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A pot mill was charged with 0.96 part by mole of BaTiO $_3$  synthesized previously, 0.05 part by mole of MgO, 0.01 part by mole of ZnO, 0.03 part by mole of TiO $_2$  and 0.005 part by mole of Ho $_2$ O $_3$ , each component having purity of 99% or higher, together with alumina balls and 2.5 liters of water, and the resulting mixture was stirred for 15 hours.

The resulting mixture was charged into a stainless pot and dried at  $150\,^{\circ}$ C for 4 hours with a hot air dryer and the dry mixture was then pulverized to rough particles which in turn were calcined in air at approximately 1,000  $^{\circ}$ C for 2 hours with a tunnel oven, thereby yielding powder of the first basic component as represented by  $(Ba_{0.96}Mg_{0.05}Zn_{0.01})O_{1.02}(Ti_{0.99}Ho_{0.01})-O_{1.995}$ .

Then, 98 parts by mole of the powder of the first component and 2 parts by mole of powder of CaZrO<sub>3</sub> as the second basic component were weighed, and 2 parts by weight of 0.20Li<sub>2</sub>O-0.60SiO<sub>2</sub>-0.04SrO-0.10MgO-0.06ZnO were added to 100 parts by weight of the mixture of the basic components, followed by addition of 15% by weight of an organic binder consisting of an aqueous solution of acrylic ester polymer, glycerin and condensed phosphate salt and then 50% by weight of water to the resulting mixture of the basic and additive components. The resulting mixture was then pulverized and mixed in a ball mill to give a slurry.

The resulting slurry was defoamed with a vacuum defoamer and then transferred to a reverse roll coater, thereby forming a thin coat on a long polyester film. The thin coat formed on the polyester film was then heated and dried at 100 °C, thereby yielding a non-sintered ceramic sheet having the film thickness of approximately 25 microns. The sheet was then cut to give a square sheet having a 10 cm side each.

On the other hand, the conductive paste for the internal electrode was prepared by dissolving 10 grams of nickel powder having an average particle size of 1.5 microns and 0.9 gram of ethyl cellulose in 9.1 grams of butyl carbitol and stirring the resulting mixture for 10 hours with a stirrer. The resulting conductive paste was then printed on one side of the non-sintered ceramic sheet through a screen having 50 patterns, each being 14 mm long and 7 mm wide, and the coat formed on the ceramic sheet was dried.

Thereafter, one non-sintered ceramic sheet was laminated on another non-sintered ceramic sheet with each printed side directed upward. In this instance, the adjacent sheets were laminated so as for the upper sheet to be superimposed on the lower sheet in a staggered arrangement by nearly half of one pattern in the lengthwise direction, thereby formulating a laminated body. Further, four non-sintered ceramic sheets having the thickness of 60 microns were laminated on each of both surfaces of the laminated body, and the sheets were pressed to attach to each other by application of the temperature of nearly 50 °C and the vertical load of approximately 40 tons. Thereafter, the resulting laminated body was then cut in a lattice form, thereby giving fifty laminated body chips.

The chips were then placed in an oven and the temperature of the atmosphere within the oven was elevated at the rate of 100 °C per hour to 600 °C, thereby burning the organic binder. Thereafter, the atmosphere in the oven was changed from the atmosphere in air to reductive atmosphere containing  $H_2$  - (2% by volume) +  $N_2$  (98% by volume).

While the oven was held in such reductive atmosphere, the laminated body chips were heated from 600 °C to the sintering temperature as high as 1,150 °C at the rate of 100 °C per hour. After they were held at the maximum temperature, i.e. 1,150 °C, for three hours, the temperature was cooled down to 600 °C from 1,150 °C at the rate of 100 °C per hour, followed by replenishing the atmosphere in the oven with atmosphere in air (oxidative atmosphere) in which the oxidation was carried out at 600 °C for 30 minutes. Thereafter, the chips were cooled down to room temperature, thereby yielding laminated sintered body chips.

The side surfaces of the laminated sintered body chip with the electrodes exposed to the outside were coated with a conductive paste consisting of zinc, glass frit and a vehicle, and then dried. The chips were then baked at 550 °C for 15 minutes in air, thereby forming zinc electrode layers and further forming a copper layer on the zinc electrode layers by means of electroless plating method, followed by the formation of Pb-Sn soldering layer thereon by means of electric plating method to thereby formulate a pair of external electrodes.

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The aforesaid procedures gave a laminated ceramic capacitor 10 having a pair of the external electrodes 16 formed on the laminated sintered body chip 15 composed of three dielectric ceramic layers 12 and two-layered inner electrodes 14, as shown in Fig. 4. The external electrode comprises a zinc electrode layer 18, a copper layer 20 formed on the zinc electrode layer 18, and a Pb-Sn soldering layer 22 formed on the copper layer 20.

It is to be noted herein that the thickness of the dielectric ceramic layer of the laminated ceramic capacitor 10 was found to be 0.02 mm and the area of the internal electrodes, which overlaps with each other, was 25 mm² (5 mm x 5 mm). It is further to be noted herein that the composition of the dielectric layer 12 subsequent to sintering is substantially identical to a combination of the compositions of the basic components and the additive component prior to sintering.

The laminated ceramic capacitor 10 was measured for its electrical characteristics. As a result, the laminated ceramic capacitor 10 was found to have a dielectric constant  $\epsilon_s$  of 3,950, dielectric loss, tan $\delta$ , of 1.2%, electrical resistivity  $\rho$  of 5.3 x 10<sup>6</sup> M  $\Omega$  ° cm, change rates of capacitance at -55°C and +125°C when based on the capacitance at 25°C, i.e.  $\Delta C_{-55}$  and  $\Delta C_{125}$ , of -10.1% and +3.5%, respectively, change rates of capacitance at -25°C and +85°C when based on the capacitance at 20°C, i.e.  $\Delta C_{-25}$  and  $\Delta C_{85}$ , of -3.6% and -4.2%, respectively, an average direct current breakdown voltage at 125°C of 64 V/micron, and a minimum direct current breakdown voltage at 125°C of 55 V/micron.

The aforesaid electrical characteristics were measured in the following manner.

Dielectric constant  $\epsilon_s$ : The capacitance was measured at the temperature of 20 °C, frequency of 1 kHz and voltage (effective value) of 1.0 V. The dielectric constant  $\epsilon_s$  is then computed from the measured value, the facing area (25 mm²) of a pair of internal electrodes 14, and the thickness (0.02 mm) of a dielectric ceramic layer 12 interposed between the pair of the internal electrodes 14.

Dielectric loss tan  $\delta$ : The dielectric loss, tan  $\delta$  (%), was measured under the same conditions as the dielectric constant  $\epsilon$ <sub>s</sub>.

Electrical resistivity  $\rho$  (M $\Omega$  • cm): The resistance value between a pair of the external electrodes was measured after application of direct current voltage of 100 V at 20 °C for 1 minute, and the electrical resistivity  $\rho$  was measured from the measured value and a dimension.

The temperature characteristics of the capacitance was measured by measuring the capacitance of an sample placed in a thermostat at the frequency of 1 kHz and the voltage (effective value) of 1.0 volt and temperatures of -55 °C, -25 °C, 0 °C, +20 °C, +25 °C, +40 °C, +60 °C, +85 °C, +105 °C and + 125 °C and computing the rates of change at each temperature relative to the capacitance at 20 °C and 25 °C.

Direct current breakdown voltage at 125 °C: Twenty samples were immersed in a silicon oil at 125 °C

and the voltage was measured immediately before each of the samples was broken down when the direct current voltage was applied to the sample at the rate of 100 volts per second, i.e. before the passage of the current of 0.1 mA. The direct current breakdown voltage at 125 °C was then computed from the measured voltage per micron of the film thickness of the dielectric layer.

### Comparative Example 1:

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The first basic component was prepared by calcining 0.05 part by mole of MgO, 0.01 part by mole of ZnO, 0.96 part by mole of BaCO<sub>3</sub>, 0.99 part by mole of TiO<sub>2</sub> and 0.005 part by mole of Ho<sub>2</sub>O<sub>3</sub> at 1,200 °C . As the second basic component was employed CaZrO<sub>3</sub>. To 98 parts by mole of the first basic component were added 2 parts by mole of the second basic component. To 100 parts by weight of the mixture of the first and second basic components were added 2 parts by weight of an additive component as represented by 0.20Li<sub>2</sub>O-0.60SiO<sub>2</sub>-0.04SrO-0.10MgO-0.06ZnO. Then, a slurry was prepared by mixing the mixture of the basic components and the additive component with an organic binder. A laminated ceramic capacitor was prepared from the resulting slurry in the same manner as in Example 1 and measured for its electrical characteristics in the same manner as in Example 1. The results are shown in Table 2 below.

### Example 2:

As the raw material for a basic component, there were employed 0.96 part by mole of BaTiO<sub>3</sub>, 0.03 part by mole of MgO, 0.01 part by mole of ZnO, 0.01 part by mole of CaCO<sub>3</sub>, 0.01 part by mole of SrCO<sub>3</sub>, 0.02 part by mole of TiO2, and 0.01 part by mole of Er2O3. To 100 parts by weight of the basic component were added 2 parts by weight of an additive component as represented by 0.30Li<sub>2</sub>O- 0.45SiO<sub>2</sub>-0.25SrO. An ceramic capacitor was prepared in the same manner as in Example and then measured for its electric characteristics. The results are shown in Table 2 below.

#### Comparative Example 2:

The first basic component was prepared by calcining 0.03 part by mole of MgO, 0.01 part by mole of ZnO, 0.01 part by mole of CaCO<sub>3</sub>, 0.01 part by weight of SrCO<sub>3</sub>, 0.96 part by mole of BaCO<sub>3</sub>, 0.98 part by mole of TiO2 and 0.01 part by mole of Er2O3 at 1,200°C. To 100 parts by weight of the basic component were added 2 parts by weight of an additive component as represented by 0.30Li<sub>2</sub>O-0.45SiO<sub>2</sub>-0.25SrO. Then, a slurry was prepared by mixing the mixture of the basic component and the additive component with an organic binder. A laminated ceramic capacitor was prepared from the resulting slurry in the same manner as in Example 1 and measured for its electrical characteristics in the same manner as in

Example 1. The results are shown in Table 2 below.

#### Example 3:

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The basic component was prepared by employing 0.98 part by mole of BaTiO<sub>3</sub>, 0.005 part by mole of MgO, 0.005 part by mole of SrCO<sub>3</sub>, 0.01 part by mole of BaCO<sub>3</sub> and 0.01 part by mole of Yb<sub>2</sub>O<sub>3</sub>, as the first basic component, and BaZrO3, as the second basic component, at the rate of 2 parts by mole of the second basic component relative to 98 parts by mole. To 100 parts by weight of the basic components were added an additive component as represented by 0.20B2O3-0.50SiO2-0.30MgO. A ceramic capacitor was prepared in substantially the same manner as in Example 1 and the electrical characteristics for the resulting ceramic capacitor were measured in the same manner as in Example 1. The results are shown in Table 2 below.

# Comparative Example 3:

The first basic component was prepared by calcining 0.005 part by mole of MgO, 0.005 part by mole of SrCO<sub>3</sub>, 0.99 part by weight of BaCO<sub>3</sub>, 0.98 part by mole of TiO<sub>2</sub> and 0.01 part by mole of Yb<sub>2</sub>O<sub>3</sub> at 1,200 °C . To 98 parts by mole of the first basic component were added 2 parts by mole of BaZrO3 as the second basic component. To 100 parts by weight of the mixture of the first and second basic components were added 0.5 part by weight of an additive component as represented by 0.20Ba<sub>2</sub>O<sub>3</sub>-0.50SiO<sub>2</sub>- 0.30MgO. Then, a slurry was prepared by mixing the mixture of the basic component

and the additive component with an organic binder. A laminated ceramic capacitor was prepared from the

resulting slurry in the same manner as in Example 1 and measured for its electrical characteristics in the same manner as in Example 1. The results are shown in Table 2 below.

### Example 4:

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The basic component was prepared by employing 0.99 part by mole of  $BaTiO_3$ , 0.025 part by mole of  $CaCO_3$ , 0.005 part by mole of  $SrCO_3$  and 0.005 part by mole of  $Dy_2O_3$  as the first basic component, and  $CaZrO_3$ , as the second component at the rate of 2 parts by mole of the second basic component relative to 98 parts by mole of the first basic component. To 100 parts by weight of the mixture of the first and second basic component were added a first additive component as represented by  $0.10B_2O_3$ - $0.50SiO_2$ -0.08BaO-0.20CaO-0.12MgO at the rate of 2 parts by weight of the first additive component as well as  $Cr_2O_3$  and  $Al_2O_3$  as second additive components at the rate each of 0.1 part by weight. A laminated ceramic capacitor was prepared in the same manner as in Example 1. The electrical characteristics for the resulting ceramic capacitor were measured in the same manner as in Example 1, and the results are shown in Table 2 below.

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# Comparative Example 4:

The first basic component was prepared by calcining 0.025 part by mole of  $CaCO_3$ , 0.005 part by mole of  $CaCO_3$ , 0.99 part by weight of  $CaCO_3$ , 0.99 part by mole of  $CaCO_3$ , 0.095 part by mole of  $CaCO_3$ , 0.99 part by mole of  $CaCO_3$ , 0.90

#### 30 Example 5:

The basic component was prepared by employing 0.96 part by mole of  $BaTiO_3$ , 0.03 part by mole of MgO, 0.02 part by mole of  $CaCO_3$ , 0.01 part by mole of  $BaCO_3$ , 0.015 part by mole of  $Y_2O_3$  and 0.005 part by mole of  $Er_2O_3$ . To 100 parts by weight of the basic component were added a first additive component as represented by  $0.25Li_2O-0.50SiO_2-0.25BaO$  at the rate of 2 parts by weight of the first additive component as well as  $Cr_2O_3$  and  $Al_2O_3$  as second additive components at the rate each of 0.1 part by weight. A laminated ceramic capacitor was prepared in the same manner as in Example 1. The electrical characteristics for the resulting ceramic capacitor were measured in the same manner as in Example 1, and the results are shown in Table 2 below.

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## Comparative Example 5:

The first basic component was prepared by calcining 0.03 part by mole of MgO, 0.02 part by mole of  $CaCO_3$ , 0.97 part by weight of  $BaCO_3$ , 0.96 part by mole of  $TiO_2$ , 0.015 part by weight of  $Y_2O_3$  and 0.005 part by mole of  $Er_2O_3$  at 1,200 °C . To 98 parts by mole of the first basic component were added 2 parts by weight of a first additive component as represented by  $0.25Li_2O_3-0.25BaO$  as well as 0.1 part by weight of  $Cr_2O_3$  and 0.9 part by weight of  $Al_2O_3$ , each serving as second additive components. Then, a slurry was prepared by mixing the mixture of the basic component and the additive components with an organic binder. A laminated ceramic capacitor was prepared from the resulting slurry in the same manner as in Example 1 and measured for its electrical characteristics in the same manner as in Example 1. The results are shown in Table 2 below.

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	Basic G	Basic Components	S					Additive Components	ts
		Rat	es of First	Rates of First Component(in mole)	mole)	la" -	:	First	Second
	Composition Formula	BaT103	MO	R203	BaO	Ti0z	Part by	Composition Ratio	Part by
							Weignt	(a) moie)	Weignt
Example 1	0.98 { (Bao, seMgo, osZno, o1) 01, o2	0.96	MgO 0.05 ZnO 0.01	Ho203 0.005	l I	0.03	2	0.20Li <sub>2</sub> O-0.60SiO <sub>2</sub> - 0.04SrO-0.10MgO- 0.06ZnO	 
Comparative Example 1	- (110. 99/NO. 01/VI. 996/TV: UZCAZIVI		."	" "	96 .0	0.99	"	"	
Example 2	(Bao. se Mgo. oa Zno. o; Cao. o; Sro. o;) 0; oa	0.96	Mg0 0.03 Zn0 0.01 Ca0 0.01 Sr0 0.01	Er20, 0.01		0.03	"	0.30Li <sub>2</sub> 0-0.45SiO <sub>2</sub> - 0.25Sr0	
Comparative Example 2	(110, 9861 0, 02) (1, 98	1	"	"	0.96	0.98	"	"	1
Example 3	0.98 { (Bac. ssMgc. ocsSrc. ocs) 01. cc	0.98	MgO 0.005 SrO 0.005	Yb202 0.01	0.01	1	0.5	0.30Mg0	1
Comparative Example 3	(110. 981DD. 02) UI. 98/7U. ULBALLUI	 	"	"	0.99	0.98	"	"	1
Example 4	0.98 (Rac. ssCac. ozsSrc. cos) 01. oz	0.99	CaO 0.025 SrO 0.005	Dy20, 0.005	1 1	1	2	0.10820s-0.50S10s- 0.08Ba0-0.20Ca0- 0.12Mg0	Cr <sub>2</sub> O <sub>3</sub> 0.1 Al <sub>2</sub> O <sub>3</sub> 0.1
Comparative Example 4	(110. \$90%0. 01/U1. \$95) TU-ULUAL U1	1 1 1	"	"	0.99	0.99	"	""	<i>"</i>
Example 5	(Bao. 97Mgo. 03 Cao. 02) 01. 02	0.96	MgO 0.03 CaD 0.02	Y <sub>2</sub> O <sub>3</sub> 0.015 Er <sub>2</sub> O <sub>3</sub> 0.005	0.01	1 1 ···	"	0.25Li*0-0.50Si0*- 0.25Ba0	Cr20s 0.1 Al20s 0.9
Comparative Example 5	11.0, 96.10, 63.44 0, 61/U; 88	] ] ]	"	"	0.97	0.96	"	"	"

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	Ulelectric   Constant	tan 8	Electrical Resistivity $\rho$	₫	ΔC125	Δ C -26	ΔCss	DC Breakdown Voltage (V/μι	DC Breakdown Voltage(V/μm)
	3	(R)	(МΩ·ст)	(%)	(%)	(%)	(%)	Average Value	Minimum
Example 1	3950	1. 2	5.3×10°	-10.1	3.5	-3.6	-4.2	6.4	55
Comparative Example 1	3890	1.0	5.6×10°	-11.0	1.3	-4.4	-4.8	6.1	2.7
Example 2	3530	1. 1	3.3×10°	- 9.	3.6	-4.5	-4.1	6 5	5.7
Comparative Example 2	3490	1.1	3.2×10°	-11.0	3.3	-5.6	-4.5	0.9	2 5
Example 3	4360	1.2	2.5×10°	-10.0	9.3	-6.3	3.6	5.8	5 1
Comparative Example 3	4300	1.1	2.7×10°	-10.7	8.9	-7.1	3.9	5 5	3.3
Example 4	3720	1.1	2.2×10 <sup>6</sup>	-9.5	5.0	-3.8	-0.6		
Comparative Example. 4	3660	1.1	1.7×10°	-10.8	4.0	-5.0	12.4	5.8	3 5
Example 5	3600	1. 2	5.0×10°	19.4	3.9	-5.5	-3.3	58	4 9
Comparative Example 5	3480	1. 1	5.4×10°	-10.5	3.7	-6.7	13.8	ប	3 3
						_			

As is apparent from Table 2 above, the results of comparison of Examples 1 with 5 to Comparative Examples 1 to 5 reveal that the minimum value of the direct current breakdown voltage of each of the ceramic capacitors at 125 °C are increased to a great extent, thereby causing a high decrease in deviation of the direct current breakdown voltage and increasing an average of the direct breakdown voltage.

The ceramic capacitors produced in Examples 1 to 5 were examined for the minute structure of their

dielectric layers.

Samples of the ceramic capacitors were analyzed for the overall distribution of elements by an X-ray microanalyser. As a result, it was found that no deviation in the large-scale distribution of the composition was recognized in any sample and that the distribution of the composition was uniform. On the other hand, the dielectric ceramic compositions prepared by conventional methods were found irregular in their overall compositions because the components M and R are localized usually as pieces as large as 1 to 2 microns.

The ceramic capacitors were further examined for the distribution of the composition of their crystal grains by means of the EDS (energy diffusion type X-ray spectroscopic analysis) using an analytical electron microscope. The analytical results reveal that a large number of crystal grains are of a so-called core-shell structure in which the composition of the inner section of the crystal grain is different from the composition of the outer section thereof.

Fig. 1 is an electron microphotograph of the sample of the ceramic capacitor prepared in Example 1. As is shown from the microphotograph of Fig. 1, a plurality of streaks of domain walls are shown in the core section (the section as indicated by the arrow A in Fig. 1) of the crystal grain. This indicates the existence of a highly dielectric domain in which the dipole moment is polarized in a particular direction. On the other hand, no such streaks are shown in the shell section (the section as indicated by the arrow B in Fig. 1) of the crystal grain.

Fig. 2 is a graph showing the EDS spectrum of the section indicated by the arrow A in Fig. 1 and Fig. 3 is a graph showing the EDS spectrum of the section indicated by the arrow B in Fig. 1. The spectra of Ba and Ti alone are shown from the EDS spectrum of the core section A of the crystal grain as shown in Fig. 2, while the spectra of Ba, Ti, Ca, Zr, Ho and so on are shown from the EDS spectrum of the shell section of the crystal grain as shown in Fig. 3. In other words, it is found that the EDS spectrum of the shell section indicates such a structure that the components M and R as well as the CaZrO<sub>3</sub> component are partially diffused in the crystal grain of BaTiO<sub>3</sub>.

As described hereinabove, the ceramic capacitor according to the present invention is such that the core section of the crystal grain is composed mainly of the BaTiO<sub>3</sub> component and the shell section of the crystal grain is composed of BaTiO<sub>3</sub> and a solid solution of the component M or R of the first basic component or composed of BaTiO<sub>3</sub> and a solid solution of the component M or R of the first basic component and ZrO<sub>2</sub> of the second basic component.

Further, samples of the ceramic capacitors prepared in Comparative Example 1 to 5 were observed for their minute structures by means of the analytical electron microscope in the same manner as described hereinabove for the samples thereof prepared in Examples 1 to 5. The microphotographs reveal that, although there are a number of crystal grains having a partial core-shell structure, the number of crystal grains having the clear core-shell structure is considerably lesser than that as shown for the samples of the ceramic capacitors prepared in Examples 1 to 5.

Fig. 5 is an electron microphotograph showing the sample of the ceramic capacitor prepared in Comparative Example. Fig. 6 is a graph showing the EDS spectrum of the section of Fig. 5 as indicated by the arrow A, and Fig. 7 is a graph showing the EDS spectrum of the section of Fig. 5 as indicated by the arrow B. As is shown in Figs. 5 and 7, no solid solution of the component R (Ho) is shown in the section B corresponding to the shell section of the sample of the Comparative Example and the distribution of the component R is found irregular. It is to be noted that the irregular distribution of the component R may cause reduction to occur in local positions, thereby leading to a deviation in characteristics, i.e. reliability at high temperatures.

As described hereinabove, it is found that the ceramic capacitors prepared in Examples 1 to 5 according to the method of the present invention are less in deviation from the direct current breakdown voltage at 125 °C and flat in temperature characteristics because they have the dielectric layers in which no deviation in the distribution of the component M or R of the first basic component is found and the shell sections of the BaTiO<sub>3</sub> crystal grains are composed mainly of ceramic material with the component R or M of the first basic component and the second basic component diffused therein.

#### Claims

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1. A ceramic capacitor having a dielectric layer composed of one or more than one dielectric ceramic composition and two or more than two internal electrodes so disposed as to interpose the dielectric layer, wherein:

the dielectric ceramic composition is composed of a sintered body of a ceramic particle whose core section is composed mainly of BaTiO<sub>3</sub> and whose shell portion is composed of BaTiO<sub>3</sub> in the form of a solid solution comprising an oxide of one or more than one element selected from Mg, Ca, Sr and

Zn and an oxide as represented by  $R_2O_3$ , wherein R is one or more than one element selected from Sc, Y, Gd, Dy, Ho, Er, Yb, Tb, Tm and Lu.

- A ceramic capacitor as claimed in claim 1, wherein the shell section of the ceramic particle is composed of ZrO<sub>2</sub> in the form of a solid solution.
  - 3. A ceramic capacitor as claimed in claim 1 or wherein the internal electrode is composed of material containing Ni as a major component.
- 10 4. A process for the preparation of a ceramic capacitor comprising:
  - a step of preparing powder of raw material as basic component;
  - a step of calcining the powder of raw material as the basic component at a predetermined temperature;
  - a step of forming a slurry by mixing the calcined raw material powder with powder of an additive component and an organic binder;
    - a step of formulating the slurry into a sheet form, thereby forming a non-sintered ceramic sheet;
  - a step of yielding a laminated body by forming a conductive paste film so as to interpose the non-sintered ceramic sheet;
    - a step of baking the laminated body in a non-oxidative atmosphere; and
    - a step of subjecting the laminated body to heat treatment in an oxidative atmosphere;

wherein the basic component comprises  $BaTiO_3$  as a major component, one or more than one oxide selected from MgO, CaO, SrO and ZnO, an oxide as represented by  $R_2O_3$ , wherein R is one or more than one element selected from Sc, y, Gd, Dy, Ho, Er, Yb, Tb, Tm and Lu, and BaO and/or  $TiO_2$  so added as to amount to  $1.00 \le k \le 1.04$ , wherein k is a ratio by mole of (Ba + Mg + Ca + Sr + Zn) to (Ti + R); and

wherein an additive component comprises  $\text{Li}_2\text{O-SiO}_2\text{-MeO}$  or  $\text{B}_2\text{O}_3\text{-SiO}_2\text{-MeO}$ , wherein Me is one or more than one element selected from Ba, Ca, Sr, Mg and Zn.

- 5. A method as claimed in claim 4, wherein the conductive paste is formed with material containing Ni as30 a major component.
  - 6. A method as claimed in claim 4 or 5, wherein the additive component comprises Cr<sub>2</sub>O<sub>3</sub> and/or Al<sub>2</sub>O<sub>3</sub>.
  - 7. A process for the preparation of a ceramic capacitor comprising:
    - a step of preparing powder of raw material as a first basic component;
    - a step of calcining the powder of raw material as the first basic component at a predetermined temperature; a step of forming a slurry by mixing the calcined first basic component with powder of raw material as a second basic component, an additive component and an organic binder;
      - a step of formulating the slurry into a sheet form, thereby forming a non-sintered ceramic sheet;
    - a step of yielding a laminated body by forming a conductive paste film so as to interpose the non-sintered ceramic sheet:
      - a step of baking the laminated body in a non-oxidative atmosphere; and
      - a step of subjecting the laminated body to heat treatment in an oxidative atmosphere;

wherein the first basic component comprises BaTiO $_3$  as a major component, one or more than one oxide selected from MgO, CaO, SrO and ZnO, an oxide as represented by R $_2$ O $_3$ , wherein R is one or more than one element selected from Sc, Y, Gd, Dy, Ho, Er, Yb, Tb, Tm and Lu, and BaO and/or TiO $_2$  so added as to amount to  $1.00 \le k \le 1.04$ , wherein k is a molar ratio of (Ba + Mg + Ca + Sr + Zn) to (Ti + R);

wherein the second basic component comprises CaZrO<sub>3</sub> and/or BaZrO<sub>3</sub>; and wherein an additive component comprises Li<sub>2</sub>O-SiO<sub>2</sub>-MeO or B<sub>2</sub>O<sub>3</sub>-SiO<sub>2</sub>-MeO, wherein Me is one or more than one element selected from Ba, Ca, Sr, Mg and Zn.

- 8. A method as claimed in claim 7, wherein the conductive paste is formed with material containing Ni as a major component.
- 9. A method as claimed inclaim 7 or 8, wherein the additive component comprises Cr<sub>2</sub>O<sub>3</sub> and/or Al<sub>2</sub>O<sub>3</sub>.

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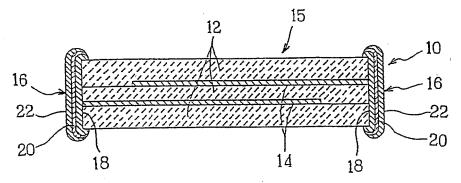
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F I G. 1



F I G. 4



F I G. 2

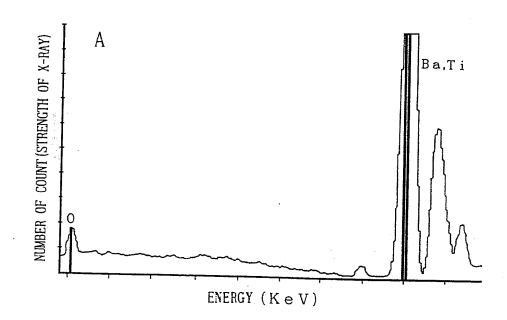
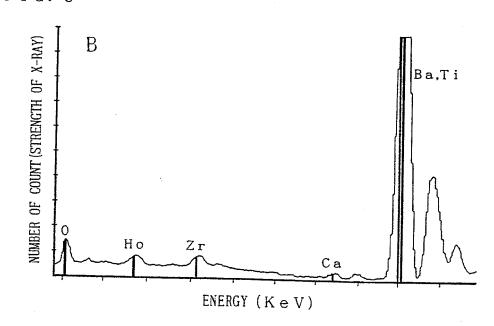


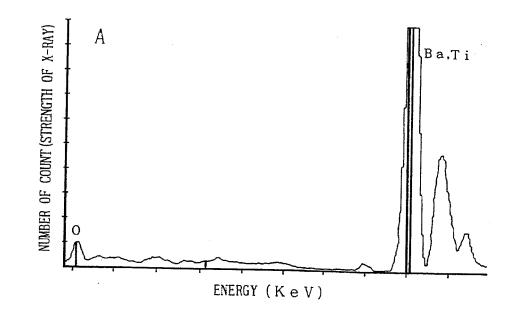
FIG. 3



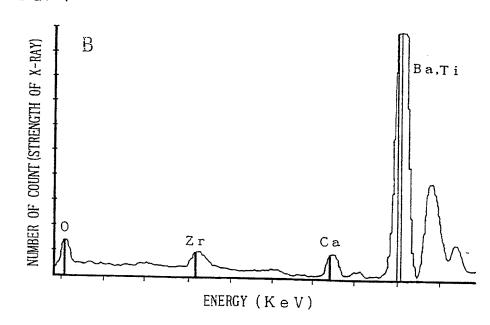
F I G. 5



FIG. 6









# **EUROPEAN SEARCH REPORT**

Application Number

EP 92 10 4372

]	DOCUMENTS CONSII	DERED TO BE RELEV	ANT	
Category	Citation of document with in of relevant pas		Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.5)
A	EP-A-0 270 098 (TAIYO YU * page 4, line 5 - page		1,4,7	H01G4/12 C04B35/46 C04B35/49
A	DE-A-4 010 827 (MURATA * claim 1 *	MFG. CO. LTD.)	4,7	CO48337 43
D,A	EP-A-0 155 366 (TAIYO YI * page 5, line 31 - line * claims 1-3 *	· · · · · · · · · · · · · · · · · · ·	4,7-8	
Р,Х	WO-A-9 113 042 (E.I. DU * page 4, line 21 - page * page 7, line 16 - page * page 17, line 16 - line	27, line 5 * 210, line 8 *	1	
	<del></del> -			
				TECHNICAL FIELDS SEARCHED (Int. Cl.5)
				H01G C04B
	The present search report has be	en drawn up for all claims		
	Place of search	Date of completion of the sear	rela .	Reminer
	THE HAGUE	17 JUNE 1992	SCH	WERMANS N.
X: par Y: par do: A: tec O: no	CATEGORY OF CITED DOCUMENT rticularly relevant if taken alone rticularly relevant if combined with and cument of the same category thrological background n-written disclosure ermediate document	E : earliér pai after the f D : document L : document	cited in the application cited for other reason of the same patent fam	blished on, or on s